

FIG. 1

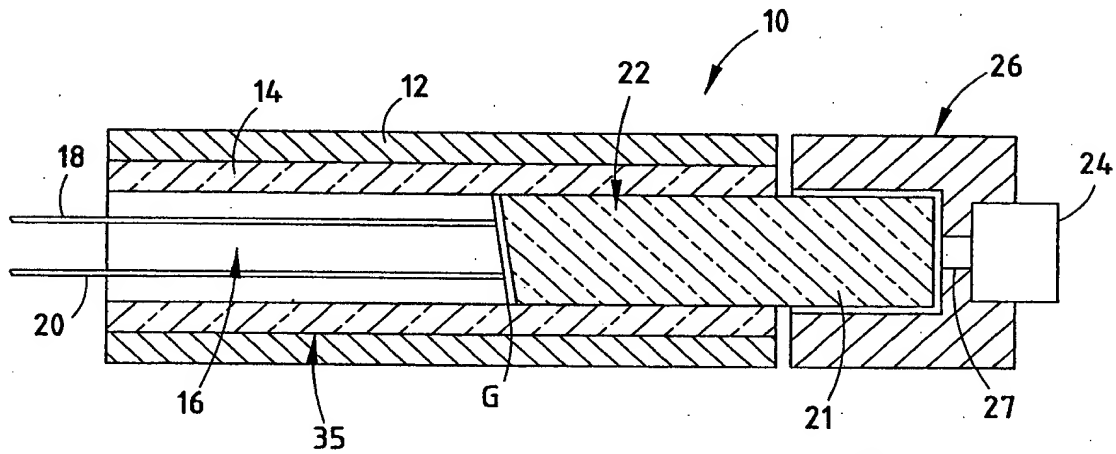


FIG. 2

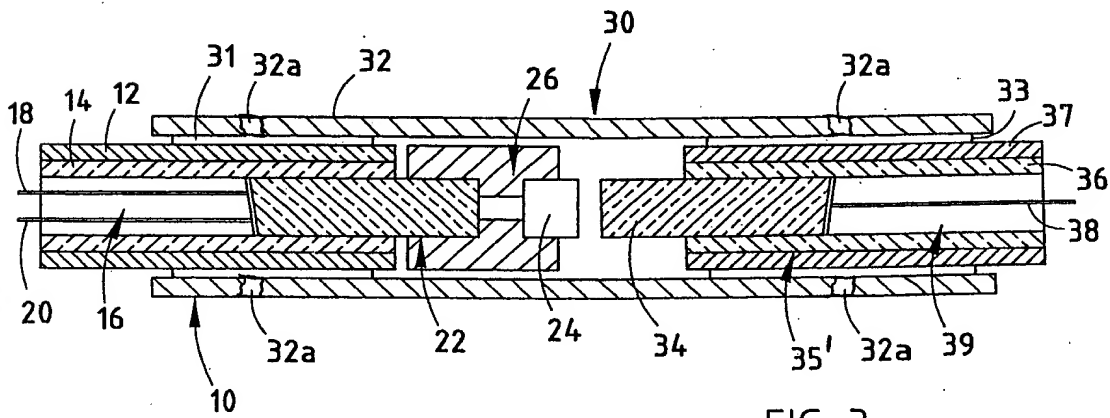


FIG. 3

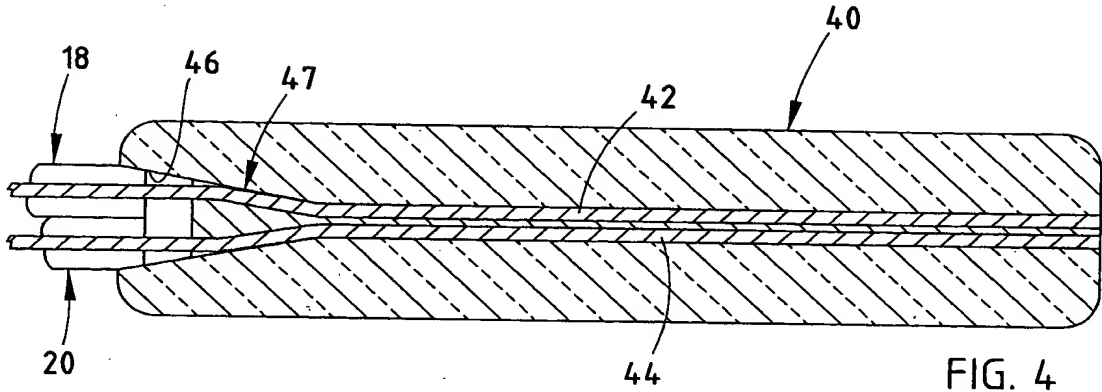


FIG. 4
(PRIOR ART)

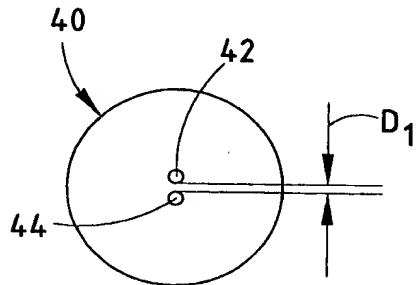


FIG. 4A
(PRIOR ART)

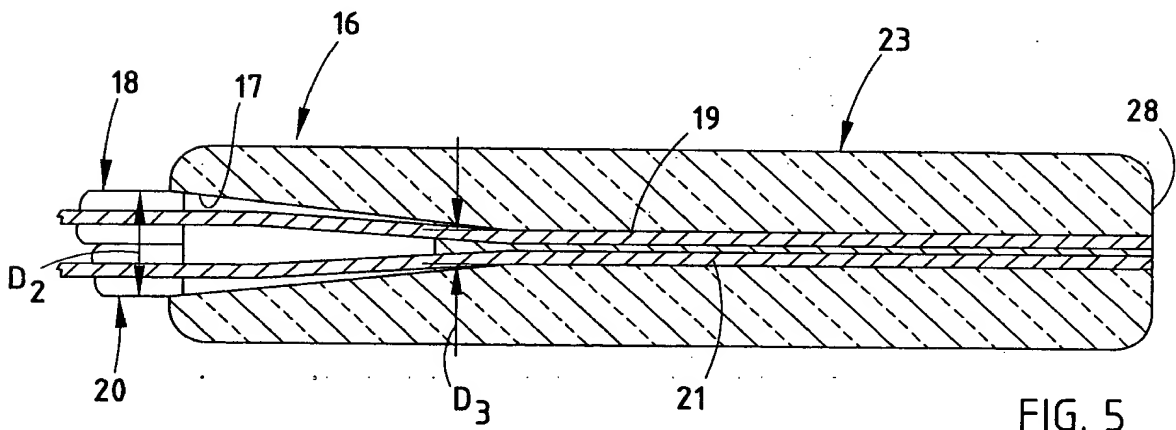


FIG. 5

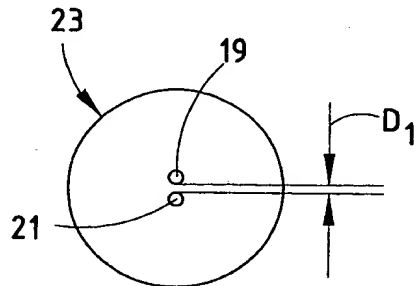


FIG. 5A

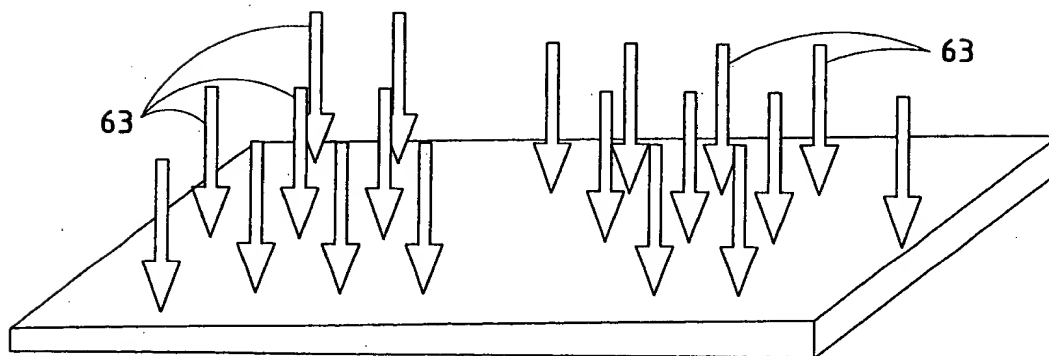


FIG. 7

MULTIPLE-PORT OPTICAL PACKAGE
AND DWDM MODULE

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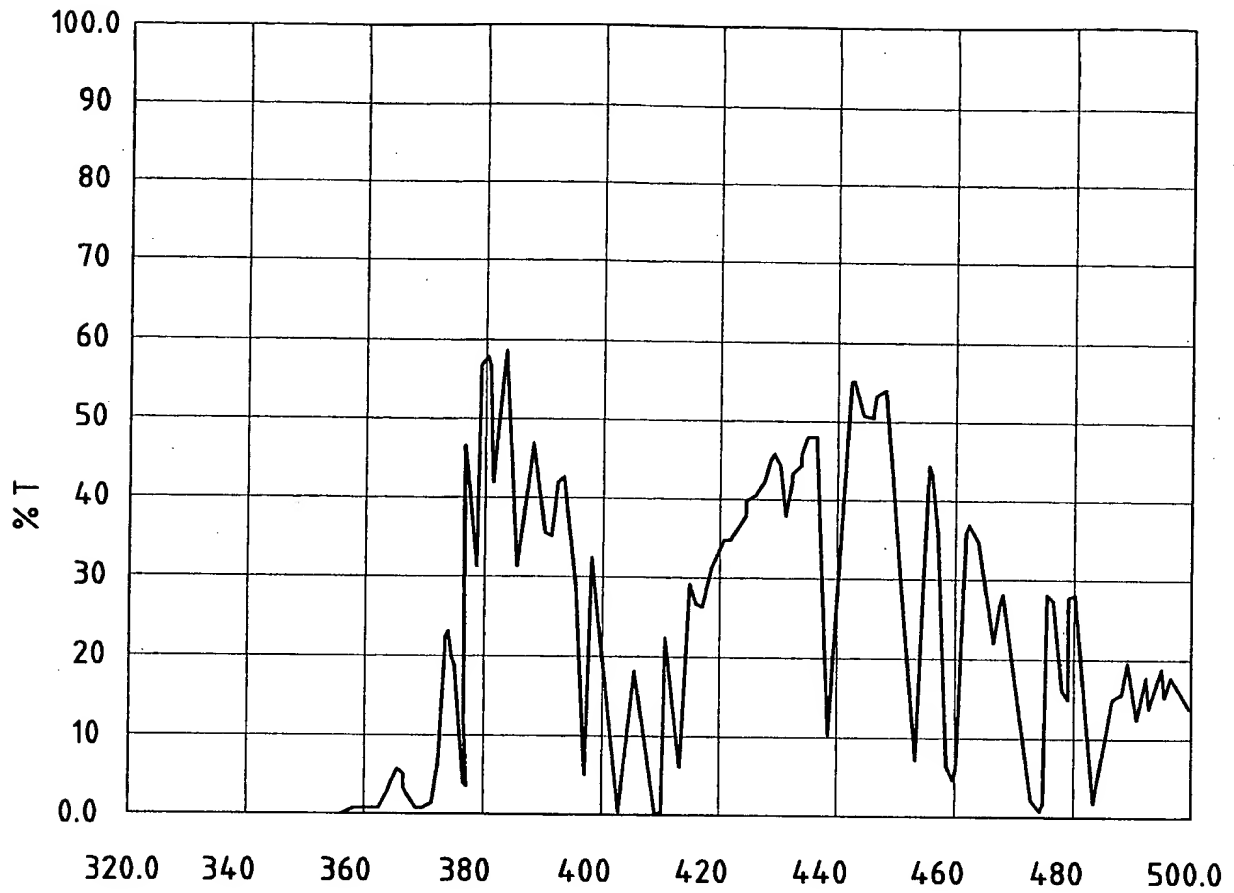


FIG. 8

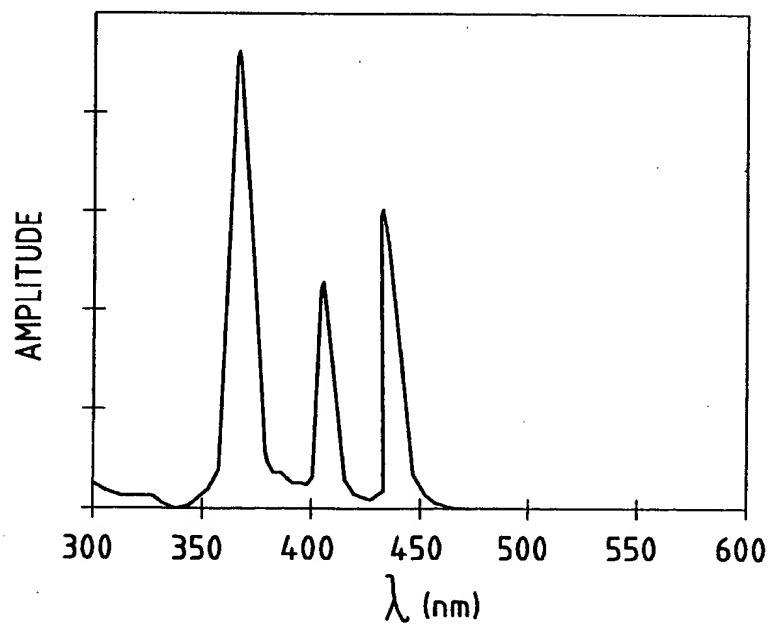


FIG. 9

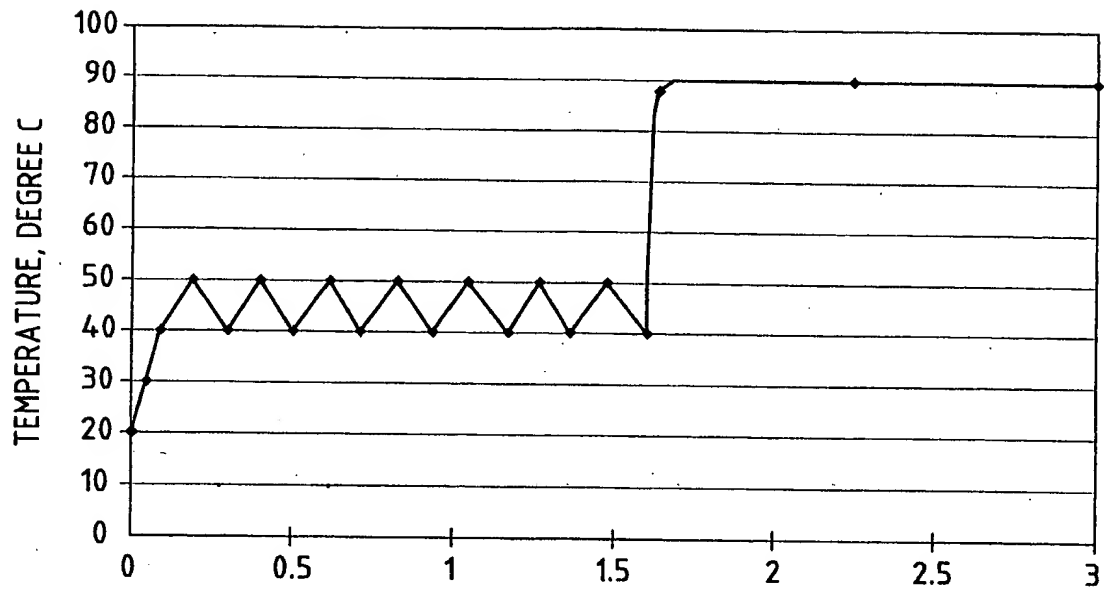


FIG. 10

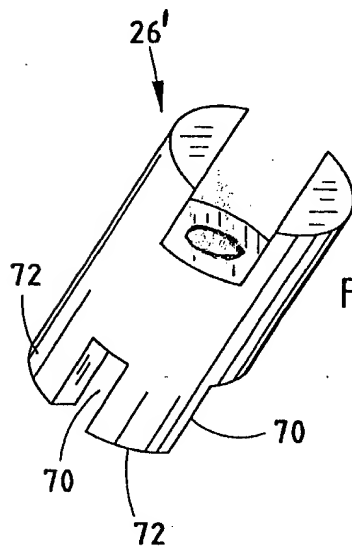


FIG. 11

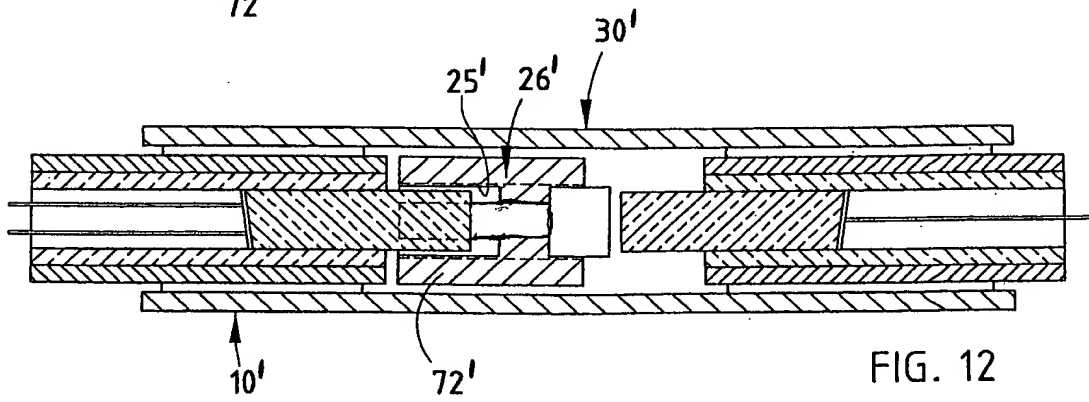
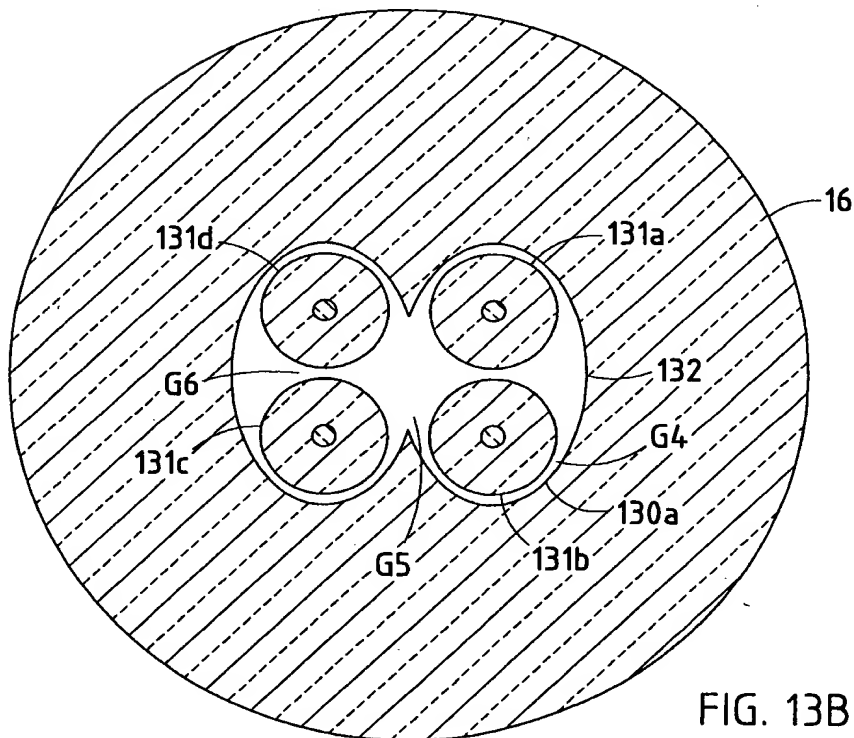
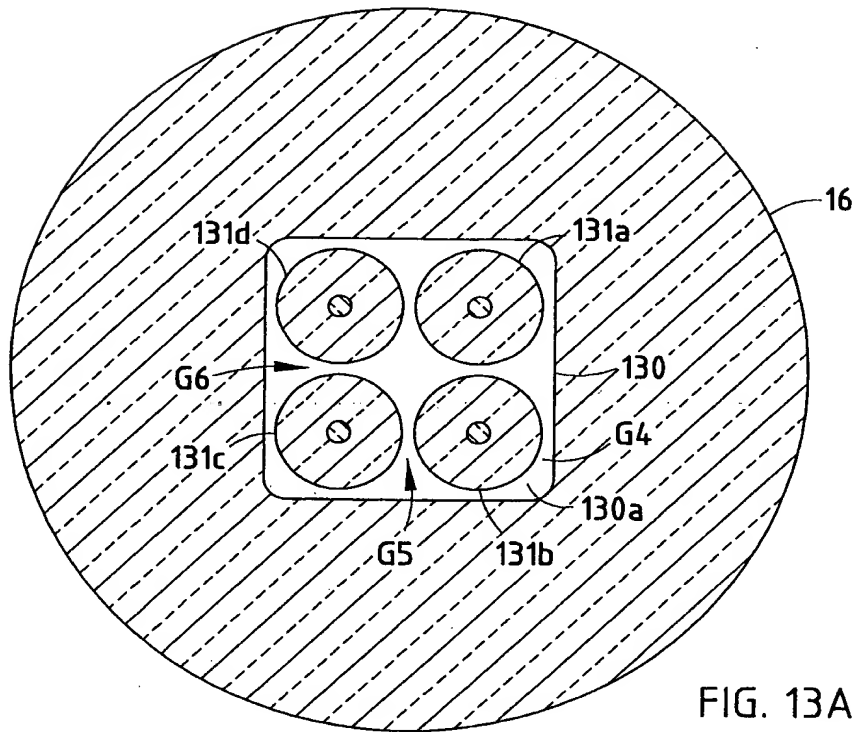


FIG. 12



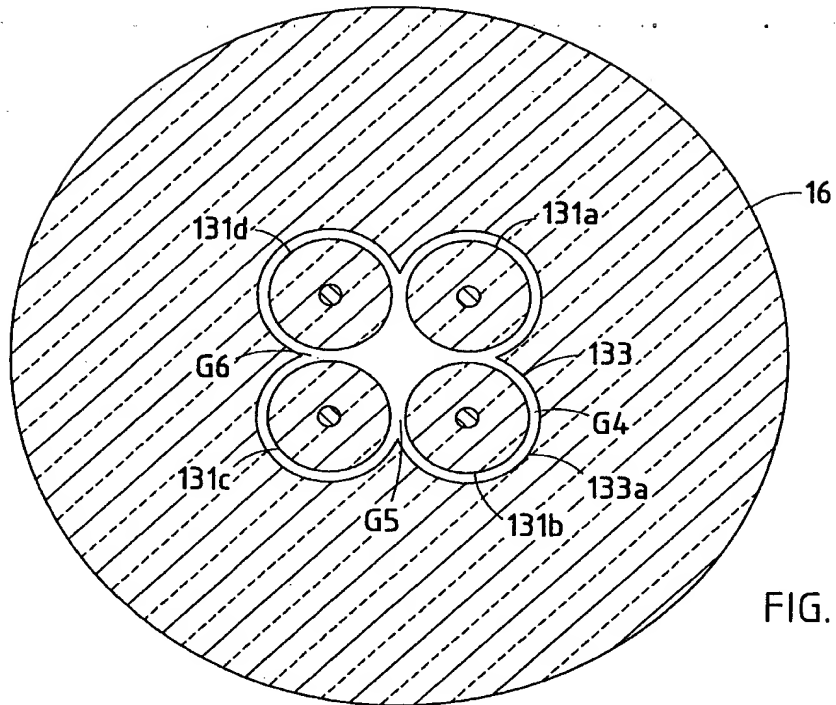


FIG. 13C

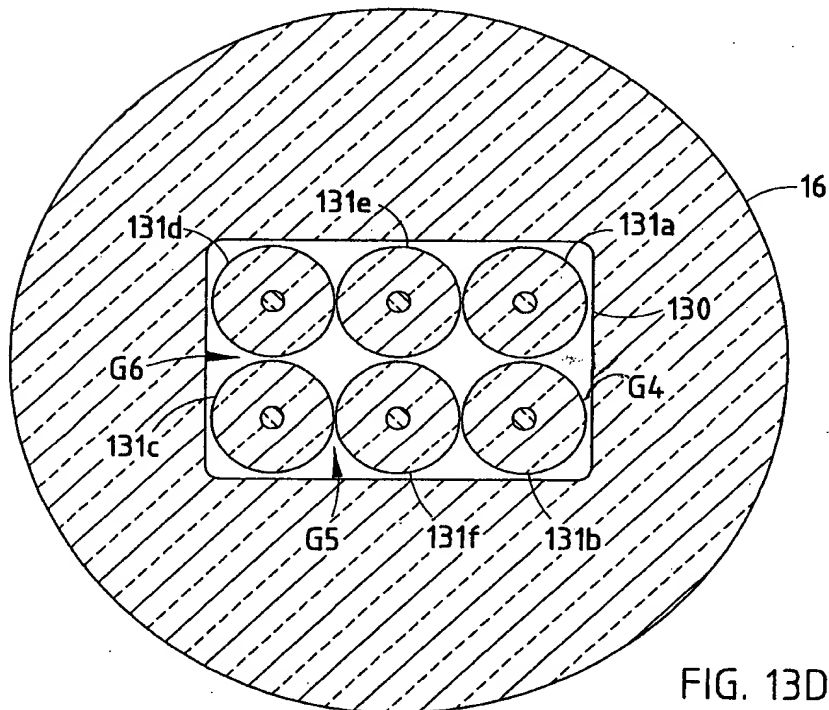
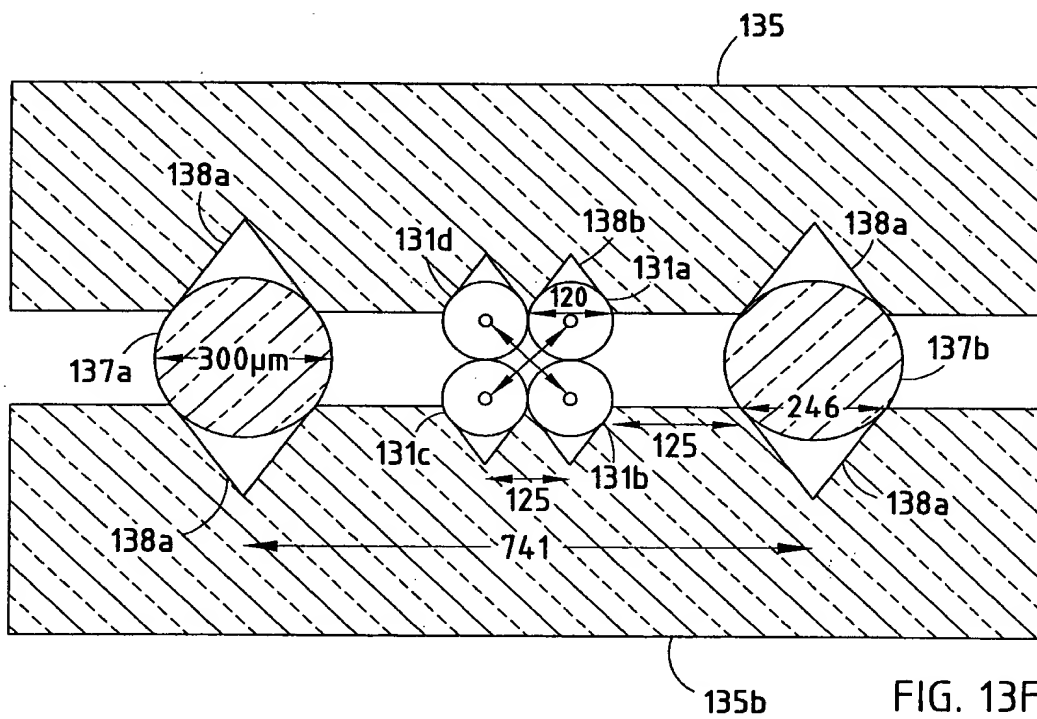
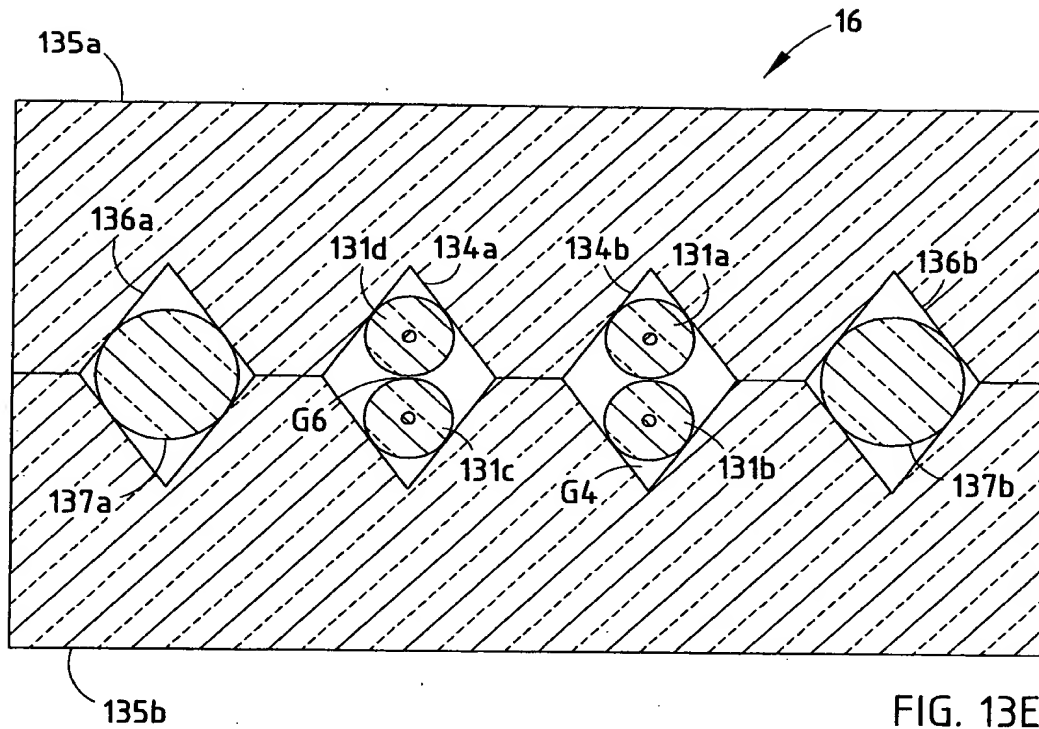


FIG. 13D



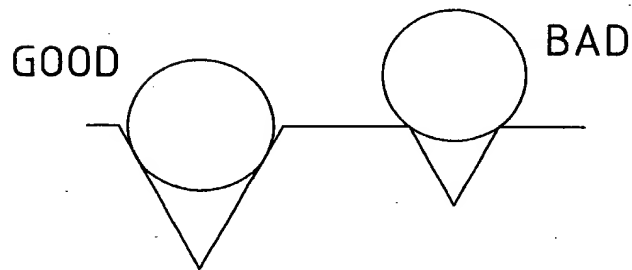
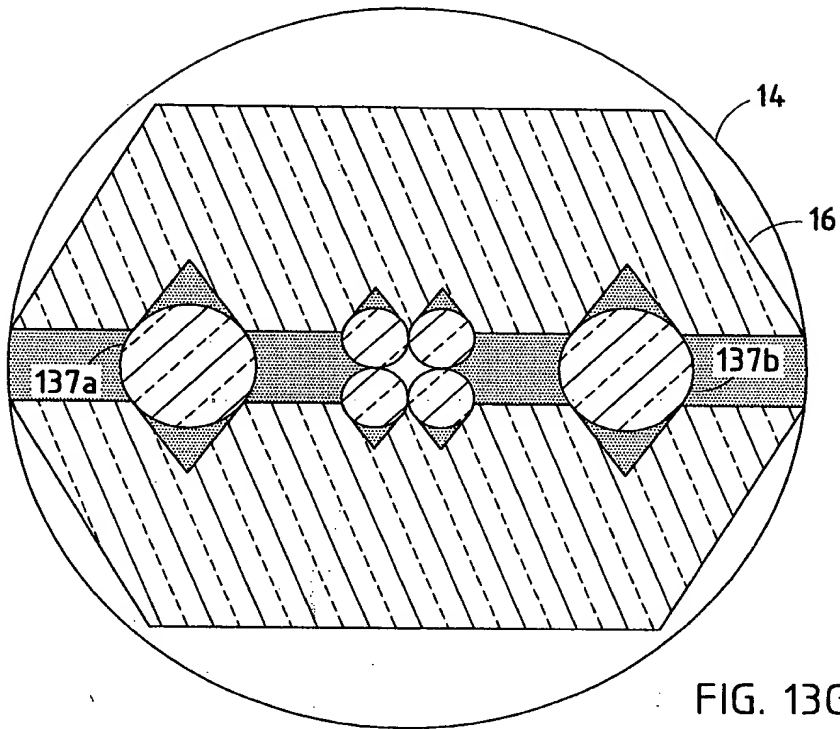


FIG. 13H

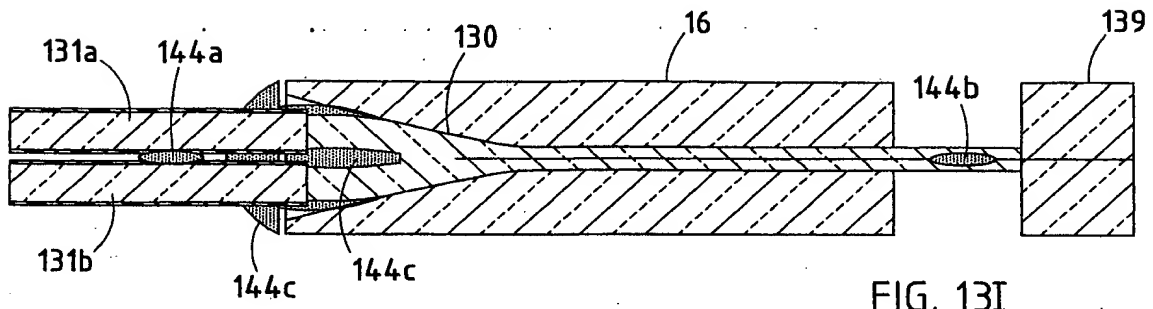


FIG. 13I

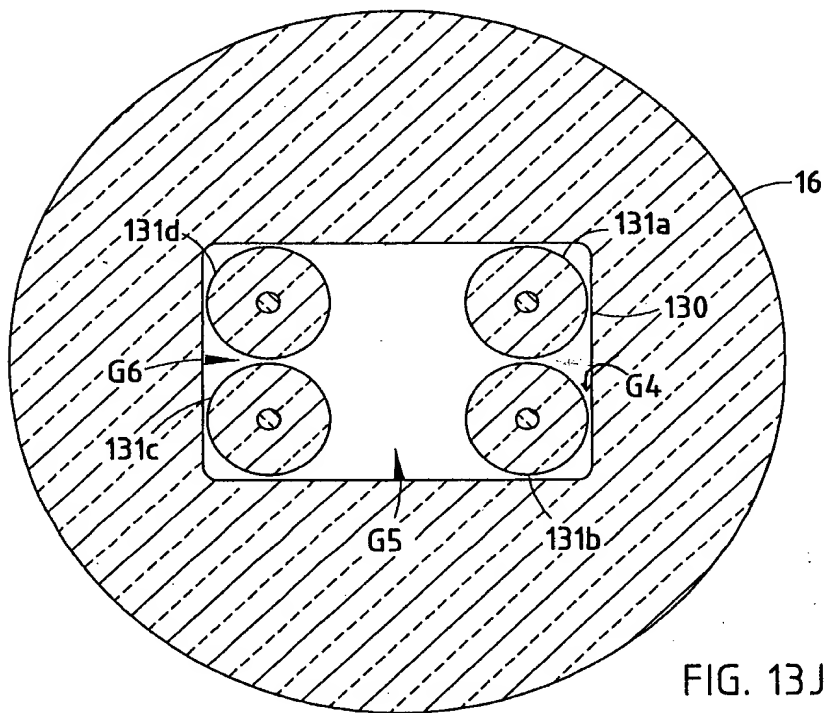


FIG. 13J

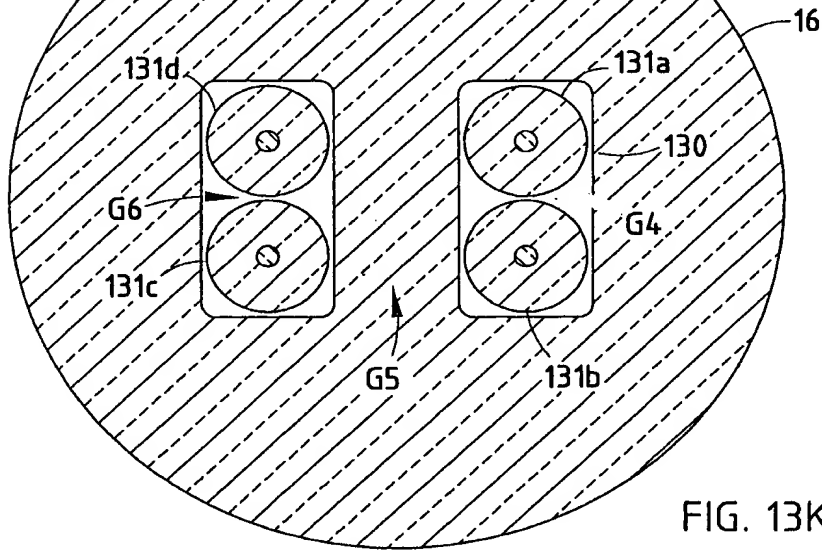


FIG. 13K

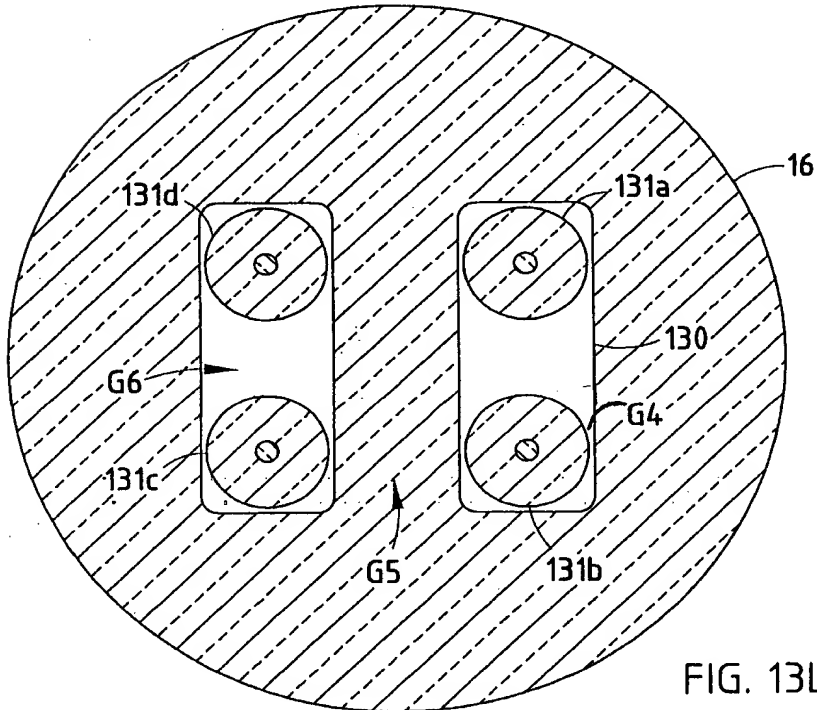


FIG. 13L

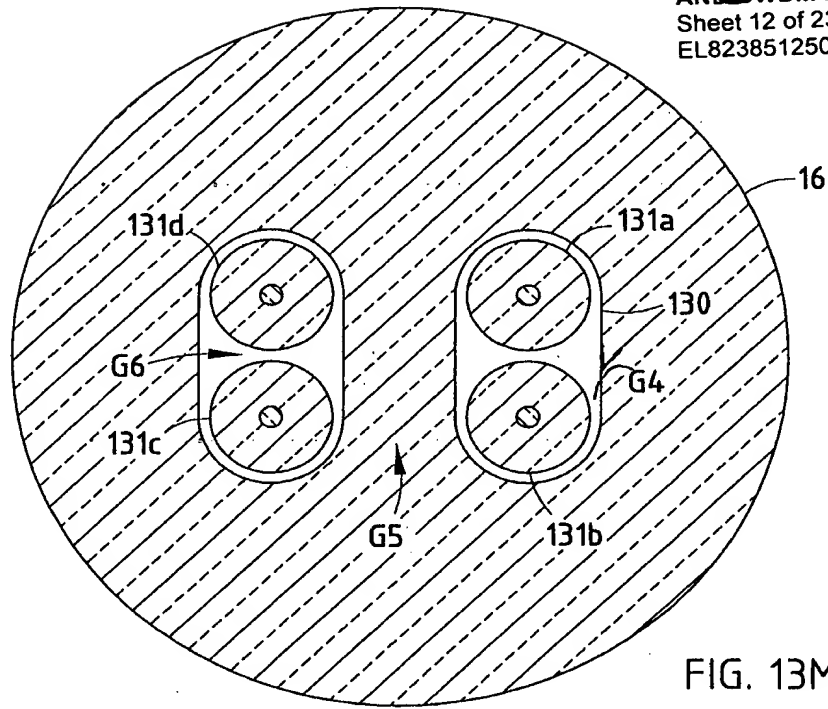


FIG. 13M

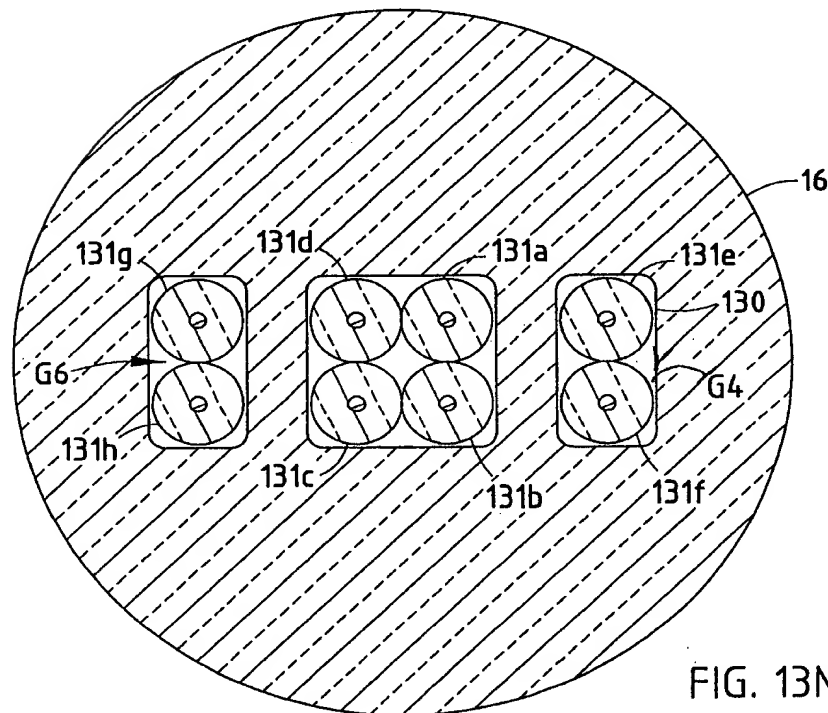
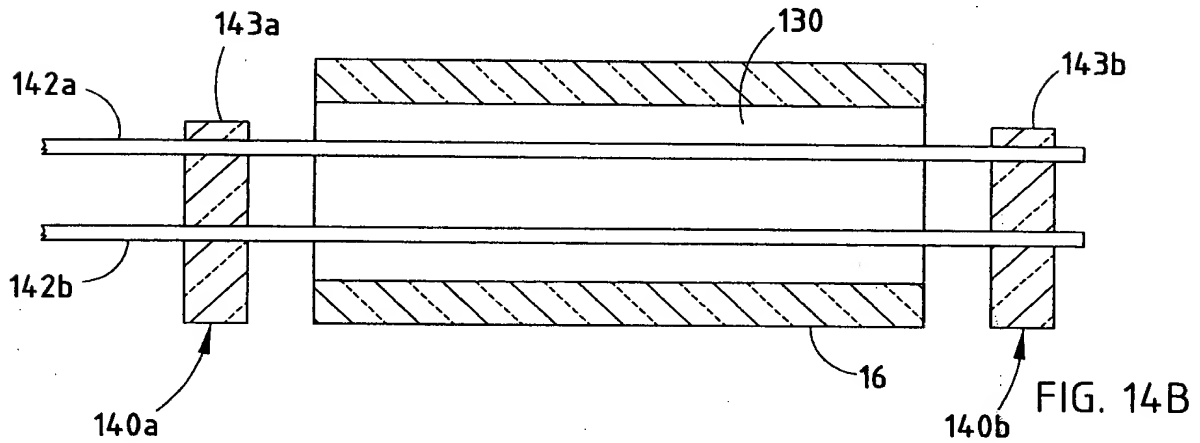
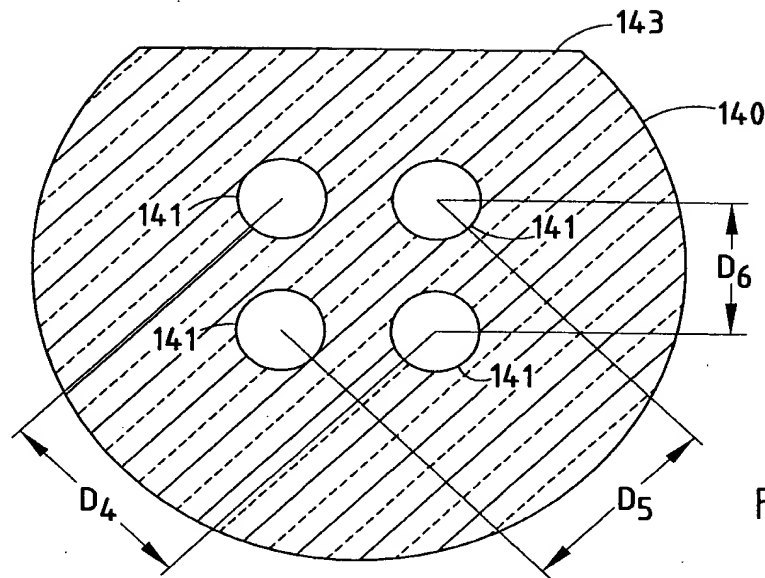


FIG. 13N



FERRULE SD	COLLIMATOR AOI	FILTER AOI RANGE
125	1.84	1.800 1.866
129	1.91	1.866 1.930
134	1.97	1.930 1.992
138	2.03	1.992 2.052
142	2.09	2.052 2.111
146	2.15	2.111 2.167
149	2.21	2.167 2.223
.	.	.
.	.	.
.	.	.

FIG. 15

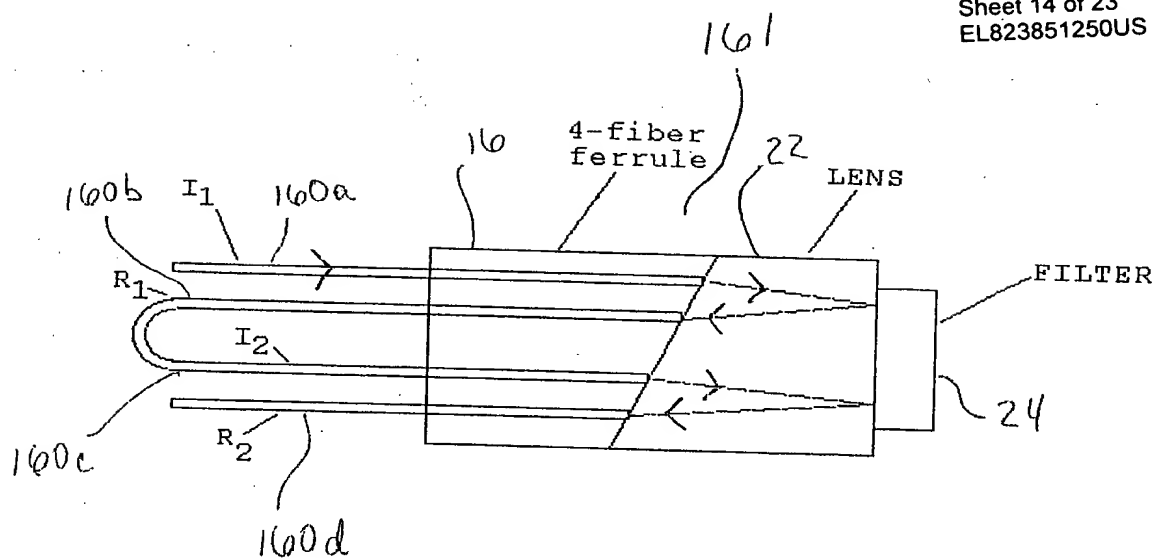


FIG. 16 A

FIG. 16 A

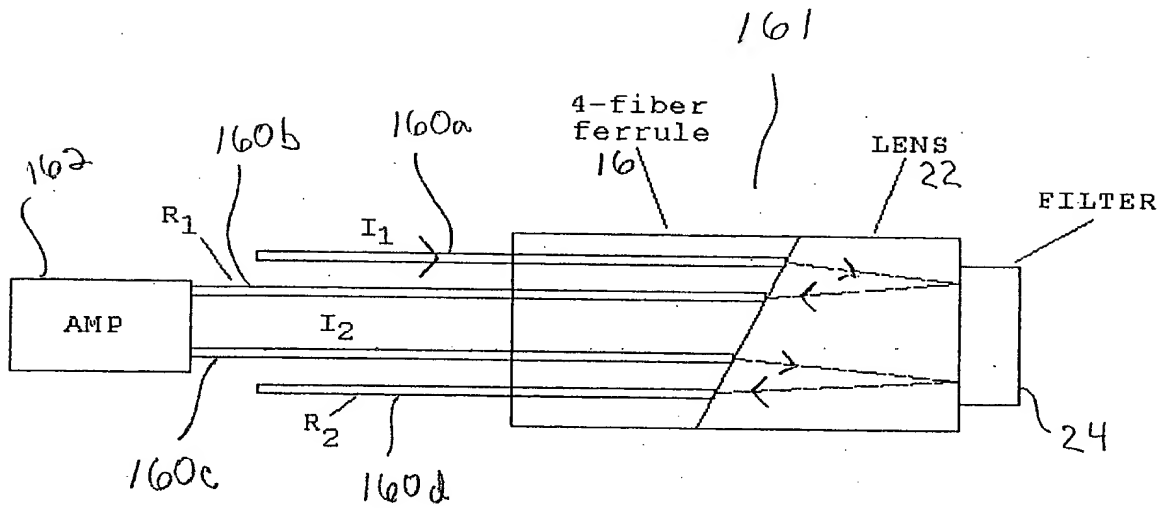


FIG. 16B

FIG. 16B

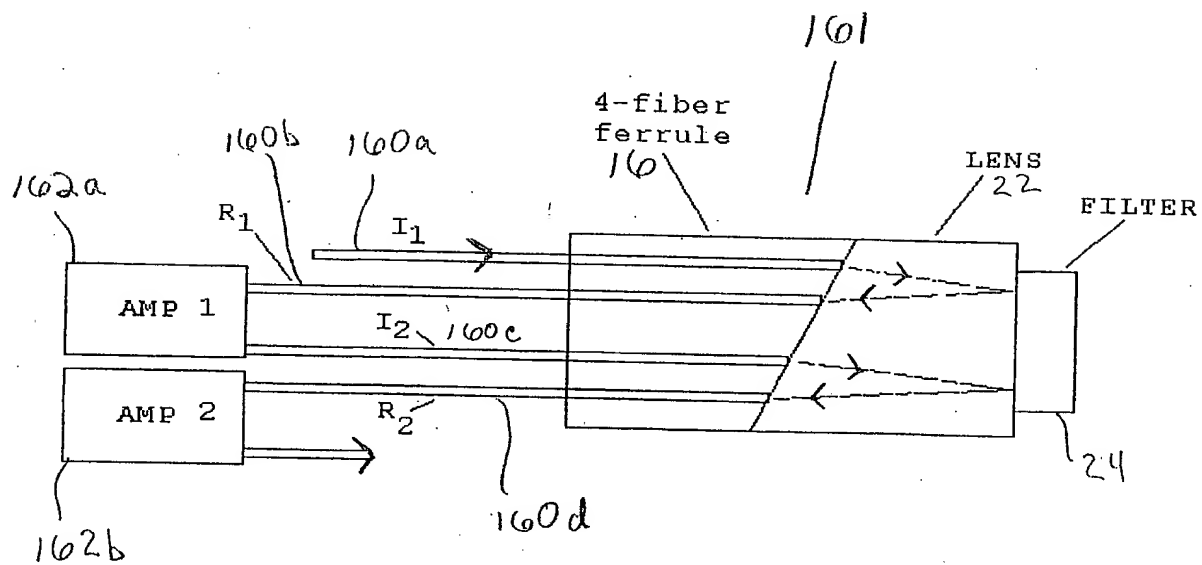


FIG. 16C

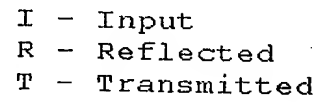


FIG. 16D

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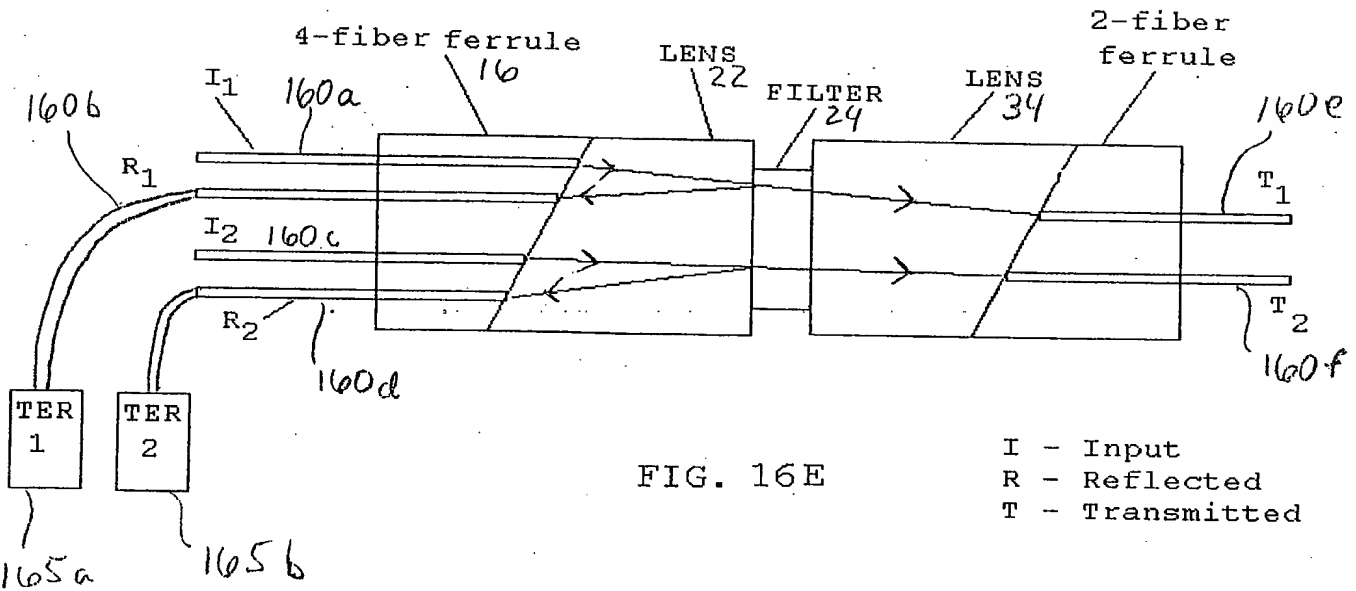


FIG. 16E

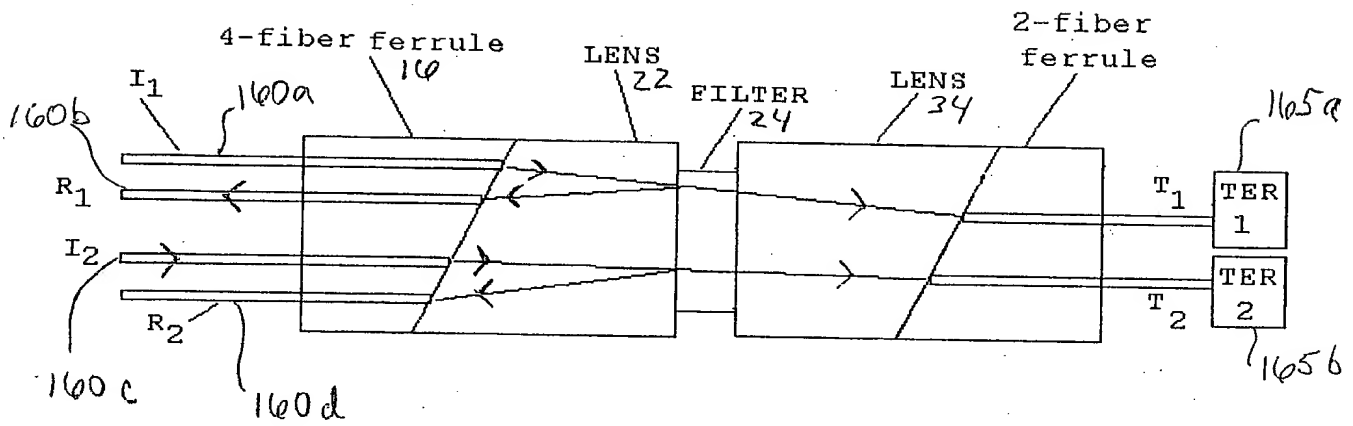


FIG. 16F

I - Input
R - Reflected
T - Transmitted

FIG. 16F

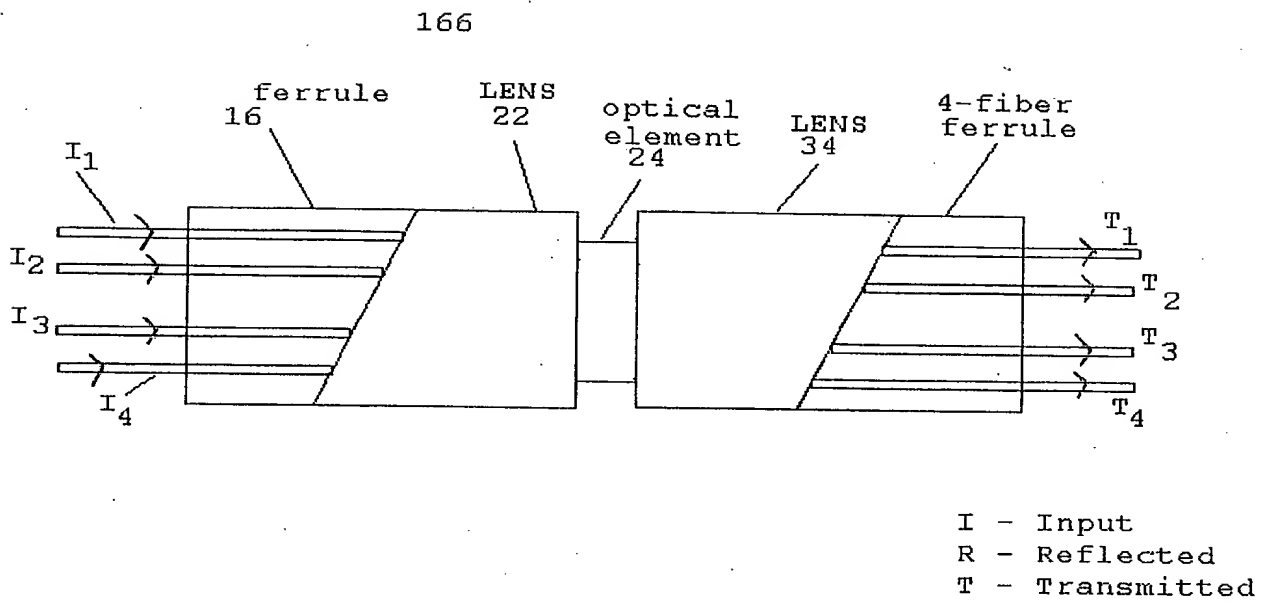


FIG. 16H

Approved for Release

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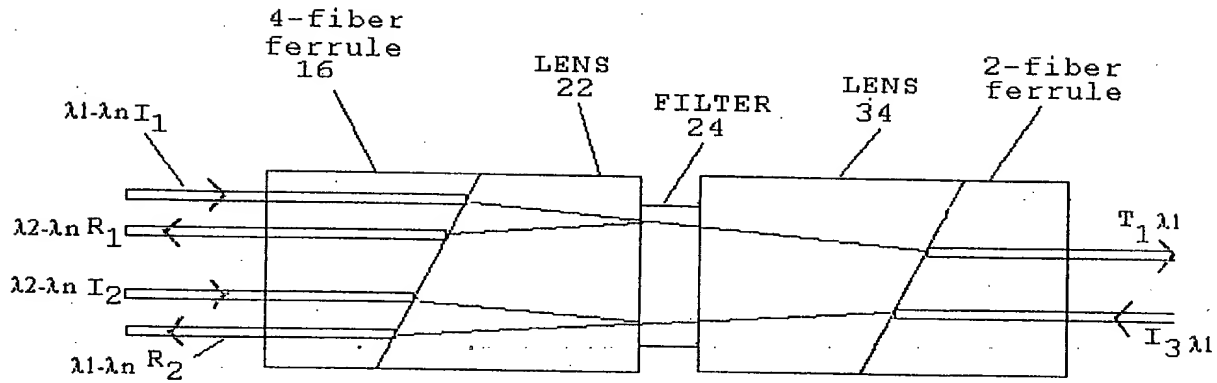
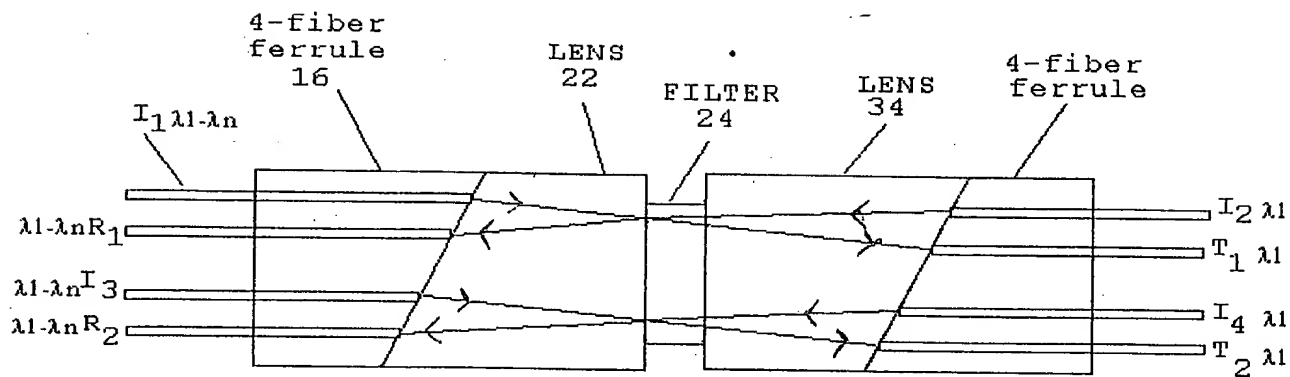


FIG. 16G

I - Input
R - Reflected
T - Transmitted

FIG. 16G is a schematic diagram of a multiple-port optical package and DWDM module.

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I - Input
R - Reflected
T - Transmitted

FIG. 16I

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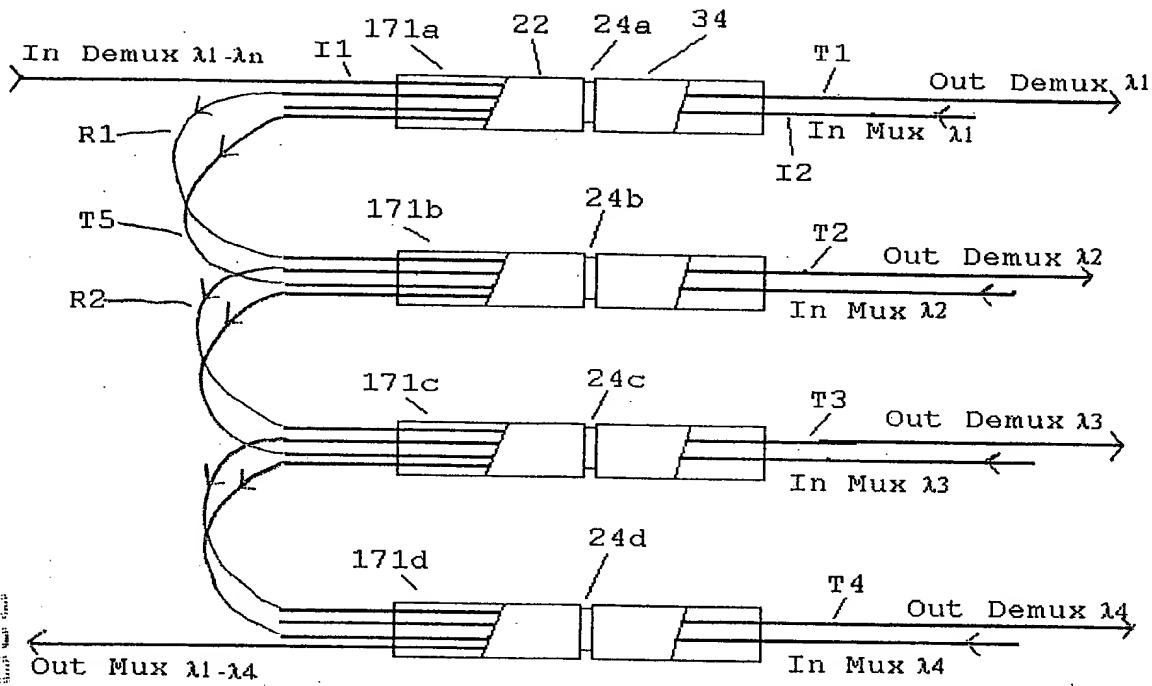


Fig. 17